

**ABSTRACT**

A single capacitor (C) can be used for both readout and noise reduction in an imaging sensor. This dual-purpose use of the single capacitor is facilitated by a switching arrangement ( $\Phi 1 - \Phi 5$ ) which connects the capacitor to a low impedance node (n7, n41) during charge storage. The low impedance node is also used to drive a column readout line ( $V_{out}$ ).